

Electronic Acknowledgement Receipt

EFS ID:	1840983
Application Number:	10500119
International Application Number:	
Confirmation Number:	7910
Title of Invention:	Connection substrate, multi-layer wiring board using the connection substrate, substrate for semiconductor package, and methods for manufacturing them
First Named Inventor/Applicant Name:	Hidehiro Nakamura
Customer Number:	20457
Filer:	William Ivan Solomon/Stacey Keaton
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Attorney Docket Number:	1204.43988X00
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	43988X00AMD.pdf	83306	no	11

Warnings:

Information:	
Total Files Size (in bytes):	83306
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